

PATENT
5201-27600/03-1237

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Hung et al.

www.pearsoned.com

Group Art Unit: Unknown
Examiner: Unknown

Serial No. 10/728,492

Atty. Dkt. No. 03-1237/5201-27600

Filed: December 5, 2003

**For: LOW POWER MEMORY
CONTROLLER WITH LEADED
DOUBLE DATA RATE DRAM
PACKAGE ARRANGED ON A
TWO LAYER PRINTED
CIRCUIT BOARD**

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313, on the date indicated below:

10/20/2004
Date

Kevin L. Daffner

PETITION UNDER 37 C.F.R. § 1.47(a)

MS: Petitions
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313

Dear Sir/Madam:

Enclosed herewith are the following:

- **Statement of Facts in Support of Filing on Behalf of Non-Signing Inventor under 37 C.F.R. § 1.47(a) with attachments;**
- **A combined Declaration and Power of Attorney executed by remaining co-inventors;**
- **A return postcard to acknowledge receipt of these materials. Please stamp and return this postcard to the undersigned.**

12/29/2006 CKHLOK 00000014 122252 10728492

01 FC:1460 130.00 DA

12/23/2006 CKHLOK 00000015 122252 10728492

01 FC:1462 400.00 DA

Please address all correspondence in connection with this application to: Customer No.
35617

The Commissioner is authorized to charge the required fees to LSI Logic Corporation
Deposit Account No. 12-2252/03-1237.

Respectfully submitted,



Kevin L. Daffer
Reg. No. 34,146
Attorney for Applicants

Conley Rose, P.C.
P.O. Box 684908
Austin, TX 78768-4908
Date: October 20, 2004